

TITLE OF INVENTION

PRINTED WIRING BOARDS HAVING LOW INDUCTANCE EMBEDDED
CAPACITORS AND METHODS OF MAKING SAME

ABSTRACT

5 A printed wiring board (PWB) has stacked innerlayer panels
comprised of passive circuit elements. The passive elements can include
capacitors with electrode terminations located within the footprints of the
capacitor electrodes. The capacitor terminations are therefore closely
spaced, reducing the capacitors' contributions to loop inductance in the
10 innerlayer. Capacitor terminations within the electrode footprints also
reduce the PWB board surface area used in forming the capacitors.